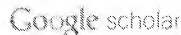


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self-routing arbitrary topology

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Deflection routing in slotted self-routing networks with arbitrary topology

IPD

CY LI, PKA Wan, YC Yuen, ... [2002, JOC 2002 IEEE](#) ... [2002 - freeexplore.ieee.org](#)

Abstract—A deflection routing algorithm that can be applied to a novel self-routing address scheme for networks with arbitrary topology is proposed. The proposed deflection routing algorithm can be implemented all-optically using bitwise optical logic gates. Besides the primary output ...

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A novel self-routing address scheme for all-optical packet-switched networks with arbitrary topologies

IPD

XC Yuan, WCK Li, CY Li, ... [Technology Journal of 2001 - freeexplore.ieee.org](#)

... For a given network topology, it is hard in general to determine the most suitable regular topology for the mapping. ... In this paper, we propose a novel self-routing address scheme that is applicable to networks with arbitrary topologies. ...

[Cited by 43](#) · [Related articles](#) · [OL Deref](#) · [All 11 versions](#)

Labelling and implicit routing in networks

N Satoru, ... [The computer journal, 1985](#) · [By Computer Soc](#)

... Efficient implicit routing algorithms are known only for networks modelled by graphs having special topologies (eg ring, hypercube). In this paper, a labelling scheme for graphs of arbitrary topology is presented, and an implicit routing algorithm is derived. ...

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... self-routing multistage interconnection network having contention-free fanout, low-loss routing, and fanin buffering to efficiently realize arbitrarily low packet loss

AZ Kuehnamdoung, ... [US Patent 5,541,914](#) (1996) [Google Patents](#)

... 30 1996 [54] PACKET-SWITCHED SELF-ROUTING MULTISTAGE INTERCONNECTION

NETWORK HAVING CONTENTION-FREE FANOUT, LOW-LOSS ROUTING, AND FANIN BLUFFERING TO EFFICIENTLY REALIZE ARBITRARILY LOW PACKET LOSS [76] inventors ...

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METANET: principles of an arbitrary topology LAN

Y Ofek, ... [Networking IEEE/ACM Transactions on, 1989](#) · [freeexplore.ieee.org](#)

... **Abstract**—The MetaNet is a scalable local area network (LAN) architecture with an arbitrary topology and a switch at each node (ie, a switch ... Following properties: 1) no packet loss due to congestion, 2) fair access to the network, 3) no deadlocks, and 4) self-routing with broadcast. ...

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Deflection routing in slotted self-routing networks with arbitrary topology

IPD

CY LI, PKA Wan, XC Yuen, ... [Selected Areas in](#) ... [2004 - freeexplore.ieee.org](#)

Abstract—A deflection routing scheme for small to medium size future all-optical networks with arbitrary topologies is proposed. The proposed scheme assumes only single-bit all-optical processing and no buffers. The primary output selection and the alternate output choices by a packet ...

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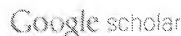
Principle for high speed network control: congestion and deadlock-freeness, self-routing, and a single buffer per link

Y Ofek, ... [Proceedings of the fourth annual ACM symposium, 1990](#) · [portal.acm.org](#)

... In this paper we present principles of operation for the basic control functions of a high-speed networkP with an arbitrary topology, we then ... and on the other hand the network access and flow control will ensure no loss, fair access to the network, no deadlocks and self-routing. ...

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A novel self-routing scheme for all-optical packet switched networks with arbitrary topology

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Efficient adaptive routing in networks of workstations with irregular topology

F. Silla, M. Maloumpe, A. Robert, P. Lopez ... and Architectural Support ... 1997 - Springer
 ... Page 13-60 27. W. Qian and L.M. Ni, "Adaptive routing in irregular networks using cut-through ...
 28. MD Schroeder et al., "Autonet: A high-speed, self-configuring local area network using
 point-to-point links," Technical Report SRC research report 59, DEC April 1990 29 ...

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A distributed reinforcement learning scheme for network routing

M. Littman ... of the 1993 international workshop on ... 1993 - [Birkbeck College Univ](#)
 ... Research Group, Bellcore Morristown, NJ Abstract In this paper we describe a self-adjusting
 algorithm ... we present an example of such an algorithm for the problem of routing packets efficiently
 in a communication network with an irregular topology and unpredictable ...

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Improving the efficiency of adaptive routing in networks with irregular topology

F. Silla ... 1997 - [computer.org](#)
 ... [8] MD Schroeder et al., "Autonet: A high-speed, self-configuring local ... [9] SL Scott and G. Thorson,
 "The Cray T3E networks' adaptive routing in a ... [10] F. Silla and J. Duato, "On the Use of Virtual
 Channels in Networks of Workstations with Irregular Topology," in Proceedings of ...

[Cited by 59](#) - [Related articles](#) - [Full Text](#) - [All 12 versions](#)

High-performance routing in networks of workstations with irregular topology

F. Silla ... Parallel and Distributed Systems, 1996 ... 2000 - [springerlink.com](#)
 ... In this paper, we propose two general methodologies for the design of adaptive routing algorithms
 for networks with irregular topology ... In this section, we propose two general methodologies for
 the design of adaptive routing algorithms for networks with irregular topology ...

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A new methodology to compute deadlock-free routing tables for irregular networks

J. Sanchez, A. Robert ... Network Based Parallel Computing ... 2000 - Springer
 ... Many NOWs are arranged as a switch-based network with irregular topology, which makes
 routing and deadlock avoidance quite complicated. Current proposals use the up / down * routing
 algorithm to remove cyclic dependencies between channels and avoid deadlock ...

[Cited by 47](#) - [Related articles](#) - [Full Text](#) - [All 8 versions](#)

On the use of virtual channels in networks of workstations with irregular topology

F. Silla ... Parallel Computer Routing and Communications, 1995 - Springer
 ... In order to make this paper self-contained, we briefly present the routing algorithm used in the
 simulations carried ... The resulting flow control protocol has been evaluated by simulation on
 networks with irregular topology, using the fully adaptive routing algorithm previously ...

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Multicast on irregular switch-based networks with wormhole routing

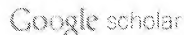
R. Keshav, K. Sundaresan ... - [hpca](#) 1997 - [computer.org](#)
 ... Switch-based networks are typically built for supporting irregular topology. Such irregularity
 provides flexibility to design scalable systems with incremental expansion capability. The
 irregularity also makes the routing on such systems quite complicated ...

[Cited by 33](#) - [Related articles](#) - [Full Text](#) - [All 12 versions](#)

A distributed self-spreading algorithm for mobile wireless sensor networks

N. Hoo ... Wireless Communications and ... 2007 - [IEEE](#)
 ... Now networks has been focused on issues such as the development of routing protocols and ... Our
 self deployment algorithm can be classified as a blanket coverage problem according to ... sensor
 nodes spend energy more evenly through the WSN than an irregular topology does ...

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Method and system for dual-network address utilization

MS Bernini · US Patent 6,716,219, 2004 · Google Patents

... 8B SELECT A PORTION OF THE DUAL PROTOCOL STACK FOR USE WITH Y-BIT ... Y-BIT NETWORK ADDRESS MAY INCLUDE A DNAT/RSP Y-BIT NETWORK ADDRESS 108 ADD A ... PACKET 110 TRANSMIT THE DATA PACKET TO THE VIRTUAL TUNNEL GATEWAY VIA A ...

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IPv6 integration and coexistence strategies for next-generation networks

M Tappanila, P Grossetete · Communications · 2004 · ieee.computer.org

... communicate with IPv4-only Web servers or dual-stack), but one drawback — well known from NAT users — is the need for dedicated application layered gateways (ALGs) when an application payload embeds an IP address. The SOCKS-based IPv4/IPv6 gateway mechanism ...

Cited by 47 · Related articles · Full Text · All 2 versions

VGSN, a gateway approach to interconnect UMTS/WLAN networks

S. Tyagi · The 13th IEEE International Symposium on 2002 · ieee.computer.org

... (2) Users with the dual mode devices subscribe a data service of one network can use ... (3) Applications use only one IP address to access Internet ... The logical entity can be either implemented as a separated node or integrated with Gateway in WLAN DT GGSN or GGSN nodes. ...

Cited by 34 · Related articles · All 8 versions

Apparatus and method for providing a secure gateway for communication and data exchanges between networks

HT Inc · US Patent 6,823,601, 1997 · Google Patents

... Users can only access public services for which an application gateway has been installed on the dual homed gateway ... all communications packets that are encapsulated with a hardware destination address that matches the device address of the gateway, b) determining ...

Cited by 430 · Related articles · All 2 versions

Marketplace for the IPv6 transition

T. Dierks · Internet Computing · IEEE · 2002 · ieee.computer.org

... The IPv6 specification states that the 32 bits after the IPv6 prefix denote the IPv4 address of the gateway for a given network. The IPv6 scenario does present some security concerns because you usually can't set filters to block packets from unauthorized sources. Dual stack. ...

Cited by 2 · Related articles · All 2 versions

Router for which a logical network address which is not unique to the router is the gateway address in default routing table entries

SE Router · US Patent App 2001/017,207, 2001 · Google Patents

... 6,2001 (54) ROUTER FOR WHICH A LOGICAL NETWORK ADDRESS WHICH IS NOT UNIQUE TO THE ROUTER IS THE GATEWAY ADDRESS IN DEFAULT ROUTING TABLE ENTRIES (76) Inventors: Scott E. Haster, Duluth, GA (US); George Morken Smith, Atlanta, GA (US) ...

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Improving gateway performance with a routing-table cache

OC Feamster · Seventh Annual Joint Conference of the ... · 1993 · ieee.computer.org

... Any gateway that is not directly connected to the destination must send the packet to the next gateway in the chain from some to destination, the address of this next gateway is the next-hop address. The gateway examines the ...

Cited by 75 · Related articles · All 2 versions

(HYM) RSP: Address sharing with end-to-end security

MS Bernini · Special Workshop on Intelligence · 2004 · ieee.computer.org

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Address translation mechanism for a high-performance network switch

M Mazzola, T Fossati ... US Patent 6,740,171, 1995 - Google Patents

... of a high performance effect forwarding decisions for the frame based on port switch is a ... Performance degradation may be accept a destination address as an input and deliver a port further ... ports i.e. store until is required (eg. on the order to kerebytes) to "dual-homed" connection ...

[Cited by 780](#) - [Related articles](#) - [All 2 versions](#)
Alerting system with dual-address memory

FR Schenck ... US Patent 4,016,461, 1977 - Google Patents

... Thus, when the page memory 32 is via line 29 by operating a momentary-contact switch interrogated, by operating the push-button 28 as ... mode which is determined by the then-existing signal gate to terminate operation of the alert signal 20 state of the dual-address memory 13 ...

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Dual mode SIMD/MIMD processor providing reuse of MIMD instruction memories as data memories when operating in SIMD mode

US Schuchman, R M, Gurek, R J, Gurek ... US Patent ... 1995 - Google Patents

... 24, 1995 [54] DUAL MODE SIMD/MIMD PROCESSOR PROVIDING REUSE OF MIMD INSTRUCTION MEMORIES AS DATA MEMORIES WHEN OPERATING IN SIMD MODE [75] Inventors: Nicholas K. King ... A crossbar switch serves to establish the processor memory links ...

[Cited by 221](#) - [Related articles](#) - [All 2 versions](#)
ATM local area network switch with dual queues

FR Laper, A, Guehen, B, Herve, J, Kerdan ... US Patent ... 1992 - Google Patents

... REG ADDRESS C IUTPUT, 49 EXPANSION OUTPUT Page 7. US Patent 5,732,087 Mar. 24, 1998 Sheet 6 of 6 CM. co no C.O no C.O Page 8, 5,732,087 1 2 ATM LOCAL AREA NETWORK SWITCH from incoming to outgoing network links. Traffic is handled WITH DUAL QUEUES ...

[Cited by 31](#) - [Related articles](#) - [All 2 versions](#)
Alerting system with dual-address memory

FR Schenck ... US Patent 4,145,654, 1978 - Google Patents

... A second operation of this switch ferminates the recalled alert signal and reacts the receiver into condition for receiving another calling ... NATURE OF THE INVENTION The present invention provides a paging receiver having both page memory and dual-address capabilities that ...

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Dual address RAM

US Chu, P C, Brown, J, Brown, J ... US Patent 4,928,498, 1990 - Google Patents

... 4,928,499 1 2 cipher 6 is representative of a plurality of write amplifi- DUAL ADDRESS RAM ers ... the same memory cell in each of the several blocks will be selected for any given address ... is connected, in parallel, to the base electrodes of a plurality of sense switch transistors 88 ...

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Telephone switch dual monitors

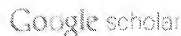
US Brockman, J I, Ayers ... US Patent 5,592,530, 1997 - Google Patents

... [6] Patent Number [45] Date of Patent: 5,592,530 Jan. 7, 1997 [54] TELEPHONE SWITCH DUAL MONITORS [75] inventors: Pierce E. Brockman, Garland, John I. Ayers, Gary S. Ruwaldt, both of Plano, all of Tex. [73] Assignee: Inet, Inc., Plano, Tex. [21] Appl. ...

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Method and system for dual-network address utilization

US Borker ... US Patent 6,732,219, 2004 - Google Patents

... networks Dual address allocation of IPv4 and/or IPv6 network addresses is pro- vided

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dual address router

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Method and system for dual-network address utilization

[M. Ruppig](#) - US Patent 6 706 216, 2004 - Google Patents

... More specifically, it relates to a method and system for dual network address utilization. ... Computational burdens placed on a network address translation router may be significant and degrade network performance, especially if several network address translation ...

[Cited by 85](#) - [Related articles](#) - [All 2 versions](#)

High bandwidth PCI to packet switched router bridge having minimized memory latency

[S.E. Miller](#) - US Patent 6 612 164, 1994 - Google Patents

... This bit is compared against address bit 57 in selecting or clearing the buffers ... The Bridge ASIC supports a dual ring arbitration scheme, allowing PCI devices to be either real ... transfers must be broken up into quarter cache transfers (on the Switched packet router interconnect) or ...

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Data router backup feature

[G.C. Hesse](#) - US Patent 5 826 666, 1998 - Google Patents

... LANs 15 and 16 may each be the well-known Ethernet Local Area Network (LAN) and routers 10-1 and 10-2 each may be, for example, the model 7000 dual ported router available from Cisco ... Thus, port 10-1 monitors LAN 16 for a message bearing the address of router 10-1 ...

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Method for assigning a dual IP address to a workstation attached on an IP data transmission network

[G. Eupert](#) - US Patent 6 694 241, 2004 - Google Patents

... SERVER REQUESTER AUTHENTICATION AUTHORIZATION OF USER ACCESS ANSWER REQUESTING SERVER UPDATING OF HOME ROUTER UPDATING OF USER PROXY FIG. 7 Page 7. US 6,684,243 B1 METHOD FOR ASSIGNING A DUAL IP ADDRESS TO A ...

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A proposal of dual Zipfian model for describing HTTP access trends and its application to address cache design

[M. Aki, N. Takahashi](#) - IEICE Transactions on ... 1998 - [IEEE Xplore](#)

... Summary: This paper proposes the Dual Zipfian Model addressing how to describe HTTP access trends in large-scale data communication networks, and discusses how to design the capacity of address cache tables in an edge router of the networks ...

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IPv6 integration and coexistence strategies for next-generation networks

[H. Lang, M. P. Wassef](#) - Communications ... 2004 - [IEEE Xplore](#)

... GRE tunnel regular communication tunnel technique only. Management overhead address GRE tunnel implementation is rarely available on hosts. Dual stack router. Tunnel endpoints can be secured using IPv4 IPsec. Required with IS-IS for IPv6 is configured over a tunnel ...

[Cited by 62](#) - [Related articles](#) - [BI Index](#) - [All 2 versions](#)

MIDI address converter and router

[B.J. Dawson](#) - US Patent 6 772 671, 2003 - Google Patents

... The amount to be added or subtracted to a MIDI address is input to the Adder as ... be loaded into the transmitter buffer of the UART 55 which begins transmission to the router at the ... The toggle flip flops used in the Controller may be improved from 74HC393 dual binary counters. ...

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Deal inter-router dual-function energy and area-efficient links for network-on-chip (NoC) architectures